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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/066,384	01/31/2002	Honkai Tam	P6402	4522

7590 09/10/2004

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EXAMINER

MAI, TAN V

ART UNIT	PAPER NUMBER
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2124

DATE MAILED: 09/10/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/066,384

Applicant(s)

TAM, HONKAI

Examiner

Tan V Mai

Art Unit

2124

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 12 April 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-28 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 17-28 is/are allowed.
- 6) ☒ Claim(s) 1,4,8,9,11 and 13 is/are rejected.
- 7) ☒ Claim(s) 2,3,5-7,10,12 and 14-16 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

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Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☒ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____

- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

Art Unit: 2124

1. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.⁷

Claims 1, 4, 8-9, 11 and 13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's Fig. 1 (Prior Art).

As per independent claim 1, Fig. 1 discloses the invention substantially as claimed, including: a plurality of propagate circuits (P3), a plurality of generate circuits (G3) and carry look ahead logic (400). It is noted that Fig. 1 does NOT disclose the claimed (1) plurality of carry-create signals "J3", and (2) plurality of carry-transmit signals "T3" **wherein the first groups of bit pairings are different from the second groups of bit pairing**; however, the examiner analyzes the Fig. 1 as follow: (1) propagate circuits having bits 11-9 & 5-3, (2) generate circuits having bits 8-6 & 2-0 are the same as ⁽¹⁾ the plurality of carry-create signals "J3", and (2) plurality of carry-transmit signals "T3". Therefore, the claim read on Applicant's Fig. 1. It would have been obvious to a person having ordinary skill in the art at the time the invention was made to design the claimed invention according to Applicant's Fig. 1 teachings because Fig. 1 discloses a carry look ahead having "block look ahead" as claimed.

As per dependent claim 4, Fig. 1 shows the claimed feature.

Due to the similarity of claims 8 and 13 to claims 1 and 4, they are rejected under a similar rationale.

As per dependent claim 9, the claim adds "means for generating the carry-create signal has a stack height of two". The feature is obvious to a logic designer to implementing the circuit in transistor level.

As per dependent claim 11, the claim adds "means for generating the carry-transmit signal has a stack height of three". The feature is obvious to a logic designer to implementing the circuit in transistor level.

3. Claims 2-3, 5-7, 10, 12 and 14-16 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Cited references are art of interest.

5. The following is an examiner's statement of reasons for allowance: the recorded references do NOT teach or suggest: (1) the detail "logic express" as recited in dependent claims 2 & 10; (2) the detail "logic express" as recited in dependent claims 3 & 12; (3) the detail structure of dependent claims 5, 7, 14 & 16 and (4) the detail "logic expresses" in independent claim 17.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tan V. Mai whose telephone number is (703) 305-9761. The examiner can normally be reached on Tue-Fri from 6:30am to 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kakali Chaki, can be reached on (703) 305-9662. The fax phone numbers for the organization where this application or proceeding is assigned are:

Official (703) 746-7239.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.



TAN V. MAI
PRIMARY EXAMINER

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